

We Claim:

1. An image capture and processing integrated circuit comprising:
an image sensor;
5 a plurality of analogue-to-digital converters (ADC's) that are connected to the image sensor to convert analogue signals generated by the image sensor into digital signals;
image processing circuitry that is connected to the ADC's to carry out image processing operations on the digital signals, and
a print head interface that is connected to the image processing circuitry to receive data
10 from the image processing circuitry and to format that data for a printhead.
2. An image capture and processing device as claimed in claim 1, which includes a memory device that is interposed between the image sensor integrated circuit and the image processing circuitry to store data relating to an image sensed by the image sensor integrated circuit.
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3. A device as claimed in claim 1, in which the image sensor integrated circuit defines a CMOS active pixel sensor array.
4. A device as claimed in claim 3, which the image sensor integrated circuit incorporates a
20 plurality of analog signal processors that are configured to carry out enhancement processes on analog signals generated by the active pixel sensor array.
5. A device as claimed in claim 3, in which the image processing circuitry includes color interpolation circuitry to interpolate pixel data.
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6. A device as claimed in claim 3, in which the image processing circuitry includes convolver circuitry that is configured to apply a convolution process to the image data.
7. A device as claimed in claim 1, in which the print head interface is configured to format
30 the data correctly for a pagewidth printhead.
8. A device as claimed in claim 1, which is a single integrated circuit.

9. A camera system which includes an image capture and processing device as claimed in claim 1.